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## Technology Center 2100

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/055,241

Filed:

October 29, 2001

Inventor(s):

Joseph E. Peck, Matthew

Novacek, Hugo A. Andrade and

Newton G. Petersen

Title:

SYSTEM AND METHOD

FOR CONFIGURING A RECONFIGURABLE

**SYSTEM** 

Examiner:

Unknown

Group/Art Unit:

2123

Atty. Dkt. No:

5150-63500

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450, on the date indicated below.

Jeffrey C. Hood

Name of Registered Representative

Signature

Date

#### INFORMATION DISCLOSURE STATEMENT

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant requests consideration of the references listed on the attached Form PTO-1449 and/or the additional information identified below in paragraph 3. A copy of each of the references listed on the Form PTO-1449 were previously submitted to or cited by the U.S. Patent and Trademark Office in U.S. Application Serial Nos. 10/034,565; 09/499,503; and 09/229,695 now Patent No. 6,311,149 to which the captioned application is seeking priority under 35 U.S.C. §120.

- 1. This Information Disclosure Statement is submitted:
  - a. within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);

			an International application; before the mailing date of a first Office Action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.
	b.		after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus:   the certification of paragraph 2 below is provided, or a fee of \$180.00 is enclosed.
	c.		after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.
2.	It is he	ereby ce	rtified:
		was cit	ich item of information contained in this Information Disclosure Statement ted in a communication from a foreign patent office in a counterpart foreign ation not more than three months prior to the filing of the Statement, or
		was cit applica making	ted in a communication from a foreign patent office in a counterpart foreign ation or, to the knowledge of the person signing the certification after g reasonable inquiry, was known to any individual designated in § 1.56 (c) than three months prior to the filing of the Statement.
3.			deration of the following additional information (including any co-pending addoned U.S. applications, prior uses and/or sales, etc.) is requested:
4.	For ea	ch non-l	English language reference listed on the attached Form PTO-1449:
		referen	ace is made to an English language translation submitted herewith, and/or
			nce is made to a foreign patent office search report (in the English language) atted herewith, and/or
			nce is made to an English language translation of a foreign patent office report submitted herewith, and/or
			ace is made to the concise explanation contained in the specification of the tapplication at page(s), and/or
		referen	nce is made to the concise explanation set forth below:
5.		Applic	ant also offers the following comments for the Examiner's consideration:

6.		Also enclosed is a copy of a foreign search re	eport citing these references.
7.		The listed documents were brought to the payment of the issue fee in the captioned communication from a foreign patent office not more than three months prior to the fixtatement. Applicant(s) request this Infoattached Form PTO-1449 be placed in the file.	case. The documents were cited in a e in a counterpart foreign application filing of this Information Disclosure formation Disclosure Statement and
8.		Applicant(s) request that the Information Dis PTO-1449 and references, which are being fi pursuant to 37 C.F.R. § 1.97(i), be placed in	iled before the grant of the patent and
	If any	required fees are missing, the Commissioner	r is authorized to charge said fees to
Meyer	tons, E	Hood, Kivlin, Kowert & Goetzel, P.C. De	eposit Account No. 50-1505/5150-
63500/	/JCH.		
			Respectfully submitted,
			O m
			Jeffrey C. Hood
			Reg. No. 35,198
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	ERTONS BOX 39	S, HOOD, KIVLIN, KOWERT & GOETZEL	, P.C.
		XAS 78767-0398	
(512)	853-880	00	
Date:	10	11/2002	

Form PTO-1449 (modified)

List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

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SERIAL NO: 10/055,241

APPLICANT: Peck et al.

FILING DATE: October 29, 2001

**GROUP: 2123** 

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EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1	4,849,880	7/18/89	Bhaskar et al.			
	A2	4,901,221	2/13/90	Kodosky et al.			
	A3	5,109,504	4/28/92	Littleton		RECE	VED
	A4	5,437,464	8/1/95	Terasima et al.			
	A5	5,497,498	3/5/96	Taylor		0CT 0 9	2003
<del>)</del>	A6	5,535,342	7/9/96	Taylor	Tec	hnology C.	-4- 04-
	A7	5,555,201	09/10/1996	Dangelo et al.	# # # # # # # # # # # # # # # # # # #	minorally CE	nter 2100
	A8	5,566,295	10/15/96	Cypher et al.		-	
	A9	5,583,749	12/10/96	Tredennick et al.			
	A10	5,603,043	02/11/1997	Taylor et al.			
	A11	5,603,043	2/11/97	Taylor et al.			
	A12	5,652,875	7/29/97	Taylor			
	A13	5,684,980	11/4/97	Casselman			
	A14	5,732,277	03/24/1998	Kodosky et al.			
,	A15	5,784,275	7/21/98	Sojoodi et al.			
	A16	5,847,955	12/98	Mitchell et al.			
<del> </del>	A17	6,219,628	04/2001	Kodosky et al.			
	A18	6,226,776	05/01/2001	Panchul et al.			
	A19	6,230,307	05/082001	Davis et al.			
	A20	6,311,149	10/30/2001	Ryan et al.			

### FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	I		DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
	A21	WO 94 10627 A	5/11/94	PCT			
	A22	WO 94 15311 A	7/7/94	PCT			
	A23	WO 96 14618 A	5/17/96	PCT			

ATTY. DOCKET NO: 5150-63500 SERIAL NO: 10/055,241 Form PTO-1449 (modified) List of Patents and Publications OCT 0 9 2003 For Applicant's Information act 0 6 2003 Disclosure Statement TRADEN'S Tedhnology Center 2100 (Use several sheets if necessary) APPLICANT: Peck et al. FILING DATE: October 29, 2001 **GROUP: 2123** OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) A24 Douglas, Bruce D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-657. A25 Labrosse, Jean, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214. A26 Labrosse, Jean, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389. A27 Operating Systems, 2<sup>nd</sup> Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415. A28 International Search Report for PCT/US 98/10916 dated 9/17/98. A29 "LabVIEW USER MANUAL for Windows", National Instruments Co., pp. 4/1-21. A30 Beguelin et al., "Visualization and Debugging in a Heterogenous Environment," IEEE, pp. 88-95. A31 Jamal et al., "The Applicability of the Visual Programming Language LabView to Large Real-World Application," IEEE, pp. 99-106. A32 Per-Arne Wiberg, "Graphical Programming of Time-Deterministic Real-Time Systems," IEEE, pp. 173-180. A33 XP0002087124 Xiao-Yu, et al., "Software Environment for WASMII: A Data Driven Machine with a Virtual Harware," Field Programmable Logic Architectures, Synthesis and Applications, 4th International Workshop on Field-Programmable Logic and Applications, FPL '94 Proceedings, Berlin Germany, 1994. A34 XP000554820 Edwards, et al., "Software acceleration using programmable hardware devices," IEEE Proceedings: Computers and Digital Techniques, Vol. 143, No. 1, January 1996, pp. 55-63. A35 XP000380758 Lesser, et al., "High Level Synthesis and Generating FPGAs with the BEDROC System," Journal of VLSI Signal Processing, Vol. 6, No. 2, August 1993, pp. 191-214. A36 International Search Report for PCT/US 98/13040 mailed 12/22/98. A37 Ade, M; Lauwereins, R; Peperstraete, J.A.; Hardware-Software Codesign with GRAPE, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping, pp. 40-47, June 1995.

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1999] Retrieved from the Internet @ http://www.esat.kuleuven.ac.be/acca.

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XAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through itation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)
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